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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/923,192	08/06/2001	Sung Hsia Kuo	COMP:0035--1/FLE P98-2162	9108

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EXAMINER

UNGAR, DANIEL M

ART UNIT PAPER NUMBER

2132

DATE MAILED: 06/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/923,192

Applicant(s)

KUO ET AL.

Examiner

Daniel M. Ungar

Art Unit

2132

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 06 August 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-50 is/are pending in the application.
- 4a) Of the above claim(s) 5, 6, 11, 14, 19, 23, 24, 26, 28, 29, 31-44, 46 and 49 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4, 7-10, 12, 13, 15-18, 20-22, 25, 27, 30, 45, 47, 48 and 50 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 06 August 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 08/06/2001.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED OFFICE ACTION

1. Claims 1-50, discounting cancelled claims, have been examined.

INFORMATION DISCLOSURE STATEMENT

2. The information disclosure statement filed 6 August 2001 fails to comply with 37 CFR 1.98(a)(2), which requires a legible copy of each cited foreign patent document; each non-patent literature publication or that portion which caused it to be listed; and all other information or that portion which caused it to be listed. It has been placed in the application file, but some of the information referred to therein, namely the non-patent literature publication "Nonvolatile Timekeeping RAM," has not been considered.

CONTINUATION

3. This application is a continuation of application number 09/136,865 filed on 19 August 1998. Preliminary amendments received 6 August 2001 are acknowledged and have been entered.

RESPONSE TO ARGUMENTS

4. Applicant's arguments with respect to claims 21, 22, and 30 have been considered but are moot in view of the new ground(s) of rejection.

CLAIM REJECTIONS - 35 U.S.C. 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

6. Claims 1-4, 7-10, 12-13, 15-18, 20-22, 25, 27, 30, 45, 47-48, 50 are rejected under 35 U.S.C. 102(b) as being anticipated by Selph et al., U.S. Patent Number 4,804,957.

7. Regarding claim 1, Selph et al. disclose a method of detecting removal of a component of an electrical system, comprising triggering a detection circuit upon removal of a component (see column 6, lines 52-68), disconnecting a counter within the detection circuit to retain data related to when the intrusion occurred (see column 12, lines 45-56; column 16, lines 28-58), and storing the retained data in non-volatile memory (see column 9, lines 38-60; column 12, lines 45-56; column 16, lines 28-58).
8. Regarding claims 2 and 3, Selph et al. disclose the component to be a hood of a computer chassis, and the electrical system to be a computer system (see abstract, column 6, lines 52-68; column 8, line 61 – column 9, line 30). Note that a microprocessor-based circuit that is used to measure, analyze, determine information, and store in random access memory is a computer.
9. Regarding claim 8, Selph et al. disclose relaying the data to an administrator (see abstract; column 3, lines 2-28).
10. Regarding claim 9, Selph et al. disclose resetting the detection circuit after storing (see column 12, lines 45-56).
11. Regarding claim 10, Selph et al. disclose presenting data to a user (see column 2, line 59 – column 3, line 28).
12. Regarding claim 12, Selph et al. disclose an indicator for acknowledged data (see column 12, lines 17-44).
13. Regarding claim 13, Selph et al. disclose the detection circuit powered by a battery (see column 3, lines 28-34; column 9, lines 61-65).
14. Regarding claim 15-17, Selph et al. disclose a method for detecting loss of power to a portion of a computer system, comprising triggering a detection circuit upon loss of power (see abstract column 16, lines 49-58), disconnecting a counter within the detection circuit to retain

data related to when the loss of power occurred (see column 12, lines 45-56; column 16, lines 49-58), and storing the retained data in non-volatile memory (see abstract; column 3, lines 12-34; column 9, lines 38-41; column 12, lines 45-56; column 16, lines 28-5). Note that a microprocessor-based circuit that is used to measure, analyze, determine information, and store in random access memory is a computer.

15. Regarding claims 21, 22, and 25, Selph et al. disclose a method for detecting removal of a component of a system, comprising generating an alarm signal (see abstract; column 3, lines 35-51; column 6, lines 52-68), using the signal to stop a clock, and recording the value of the clock (see column 12, lines 45-56; column 16, lines 28-58).

16. Regarding claim 27, Selph et al. disclose displaying a warning to a user upon power on self test (see column 11, lines 1-16)

17. Regarding claim 30, Selph et al. disclose the detection circuit powered by a battery (see column 3, lines 28-34; column 9, lines 61-65).

18. Regarding claim 45, Selph et al. disclose a computer system comprising:
chassis with a removable cover providing internal access to the chassis, the chassis housing internal components of the computer (see column 6, lines 52-69).
a microprocessor operatively connected to detect inputs from an input device, and memory which is connected to be read/write accessible by the microprocessor (see column 2, line 59 – column 3, line 11);
one or more devices for mass storage of data, and an output device operatively connected to receive outputs from the microprocessor (see column 9, lines 38-60);
one or more power supplies connected to provide power to the internal components (see column 9, lines 61-67); and
a detection circuit which stores data related to when the removable cover is removed (see column 6, lines 52-69; column 10, lines 17-36; column 11, lines 30-46; column 12, lines 45-56; column 16, lines 28-58).

19. Regarding claim 50, Selph et al. disclose the detection circuit powered by a battery (see column 3, lines 28-34; column 9, lines 61-65).

CLAIM REJECTIONS - 35 U.S.C. 103(a)

20. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

21. Claims 4, 7, 18, 20, 47, and 48 are rejected under 35 U.S.C. 103(a) as being unpatentable over Selph et al., as established above, in view of Cummings et al., U.S. Patent Number 5,406,260.

22. Regarding claims 4, 7, 18, 20, 47, and 48, although Selph et al. disclose a component triggering the detection circuit and storing data related to the removal of the component, they do not disclose multiple components triggering the detection circuit, or storing data related to more than one component. Nevertheless, Cummings et al. disclose each component being operable to trigger the detection unit (see abstract; column 6, lines 24-35). In the system of Selph et al., which deals with detecting intrusions, it would have been an obvious modification to one of ordinary skill in the art at the time of the invention to specify in which component the intrusion occurred by including a detection circuit for each component, as taught by Cummings et al.

CONCLUSION

23. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel M. Ungar whose telephone number is 571.272.7960. The examiner can normally be reached on 8:30 - 6:00 Monday - Thursday, Alt. Fridays.

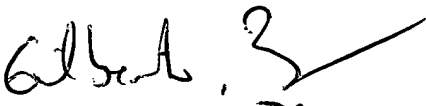
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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on 571.272.3799. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Daniel Ungar


GILBERTO BARRON JR.
SUPERVISORY PATENT EXAMINER
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